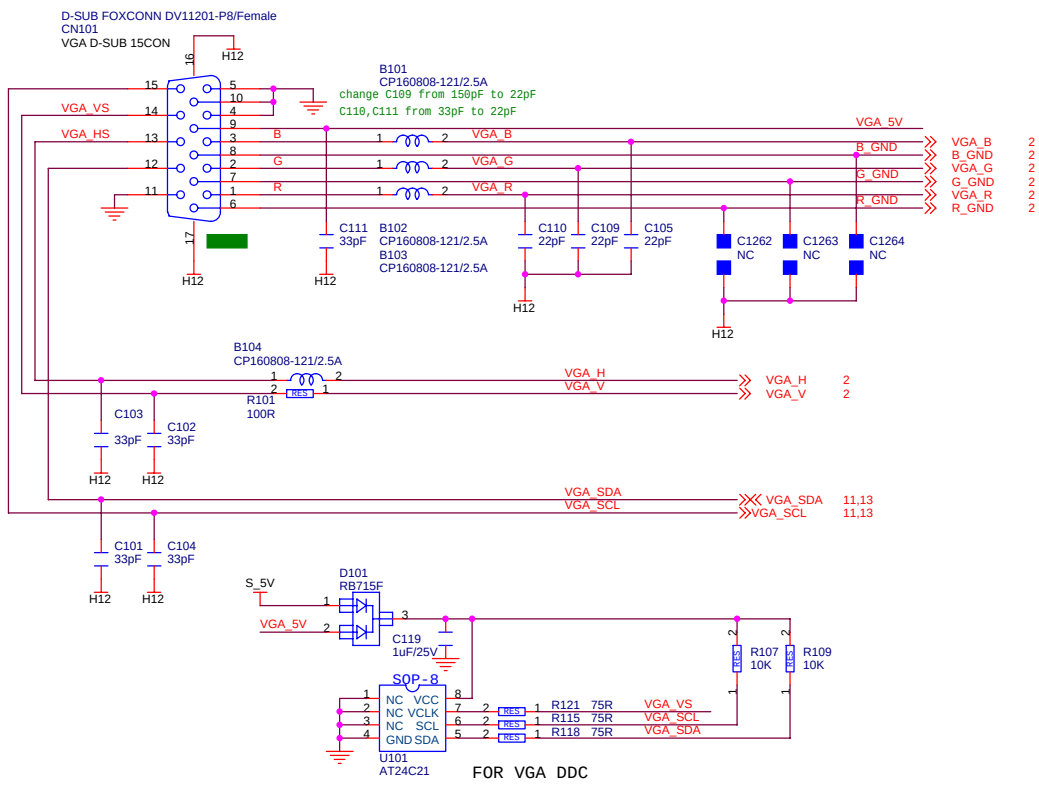
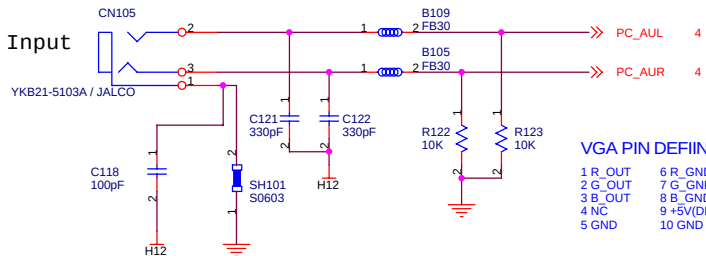




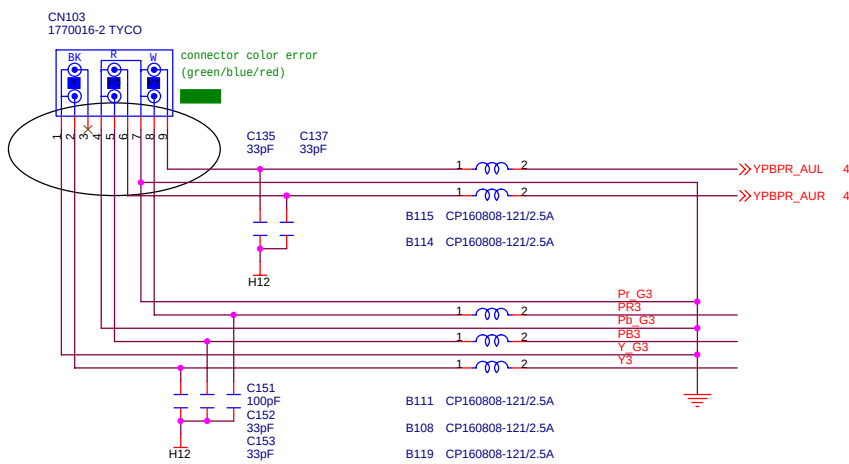
PC Audio Input



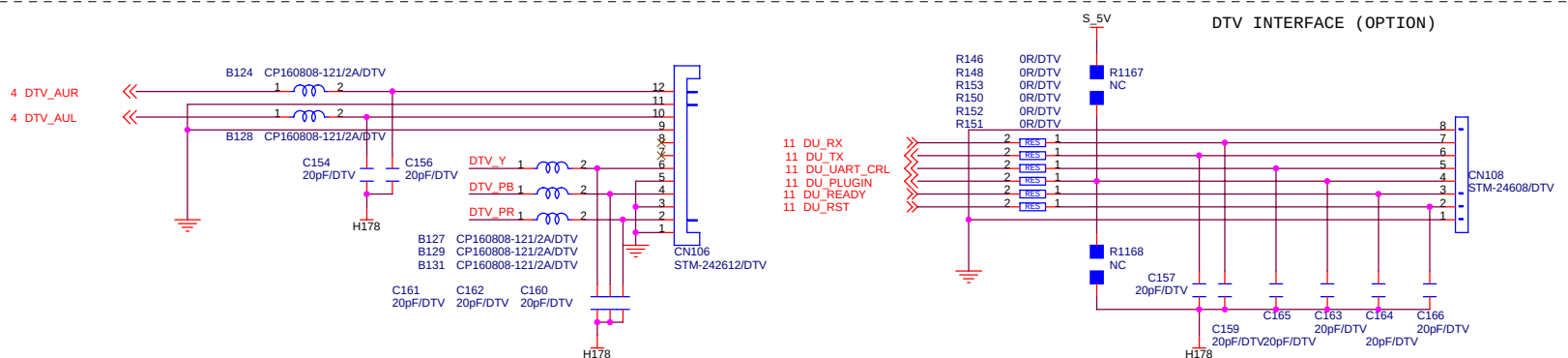
VGA PIN DEFINITION

1 R_OUT	6 R_GND	11 NC
2 G_OUT	7 G_GND	12 DDC SDA
3 B_OUT	8 B_GND	13 TTL H-S
4 NC	9 +5V(DDC)	14 TTL V-S
5 GND	10 GND	15 DDC SCL

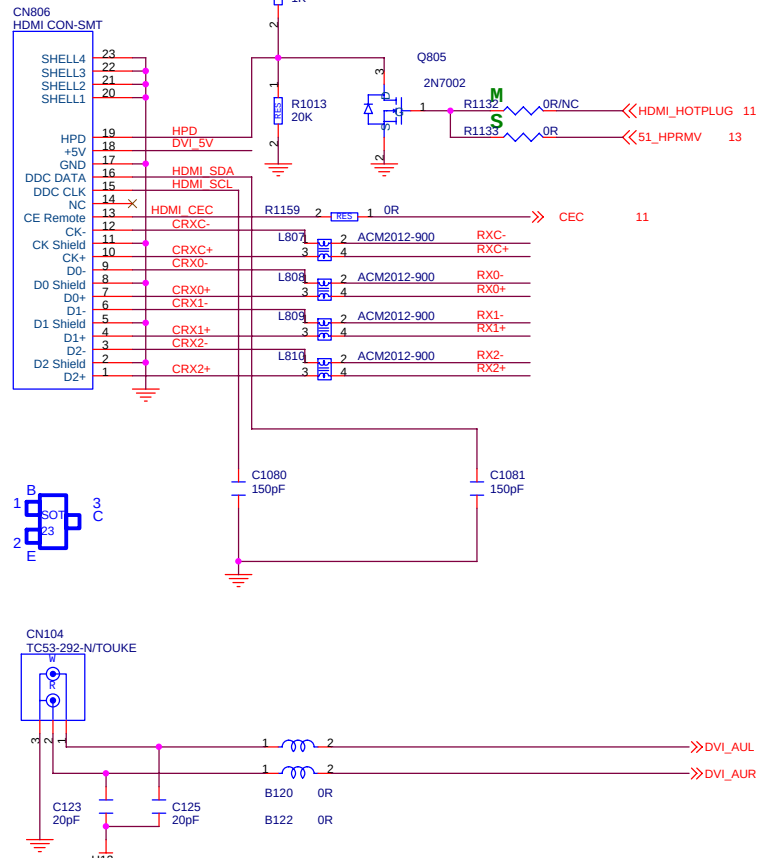
VGA INTERFACE



YPBPR INTERFACE

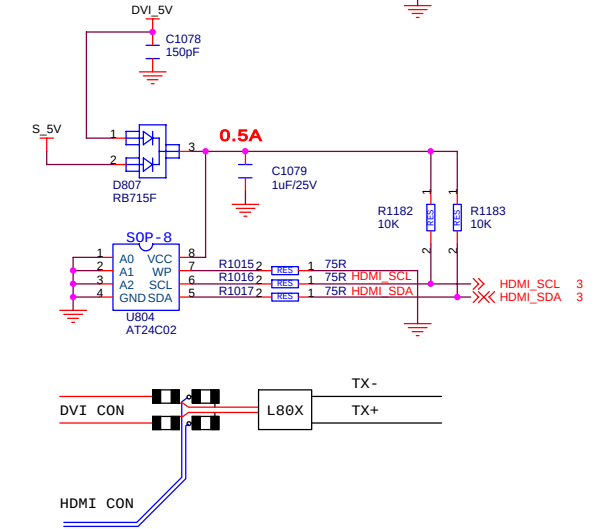


DTV INTERFACE



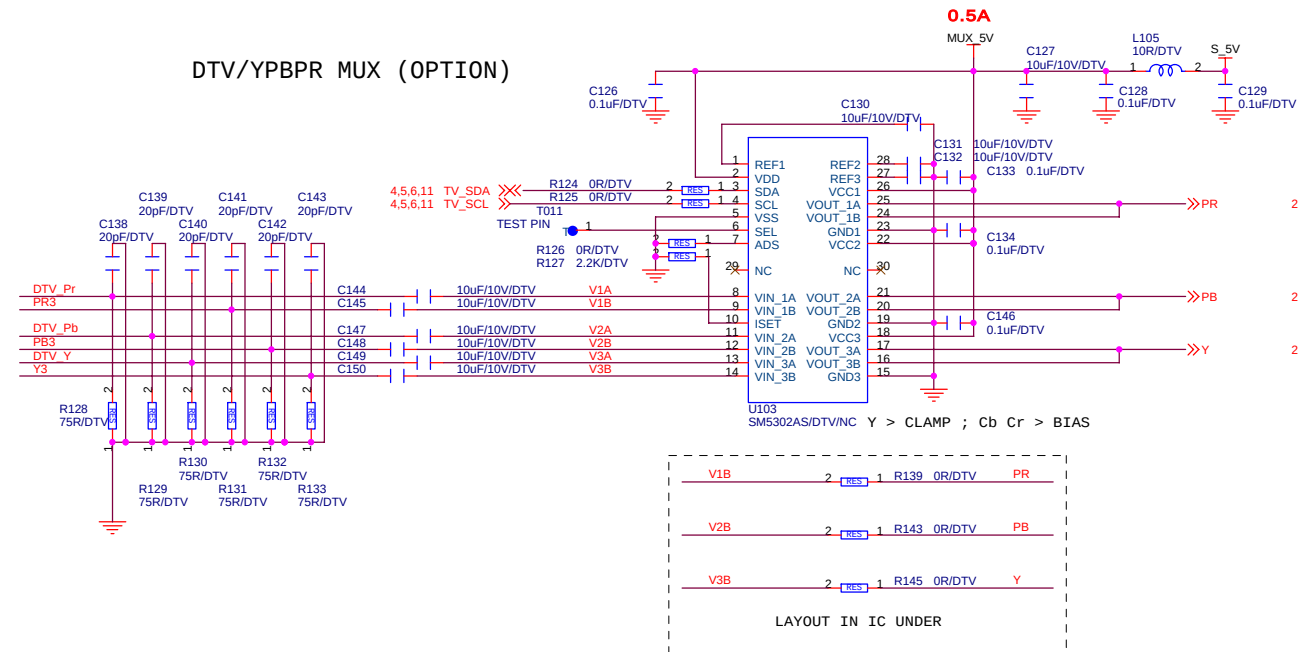
RXC-
RXC+
RX0-
RX0+
RX1-
RX1+
RX2-
RX2+

3
3
3
3
3
3
3
3

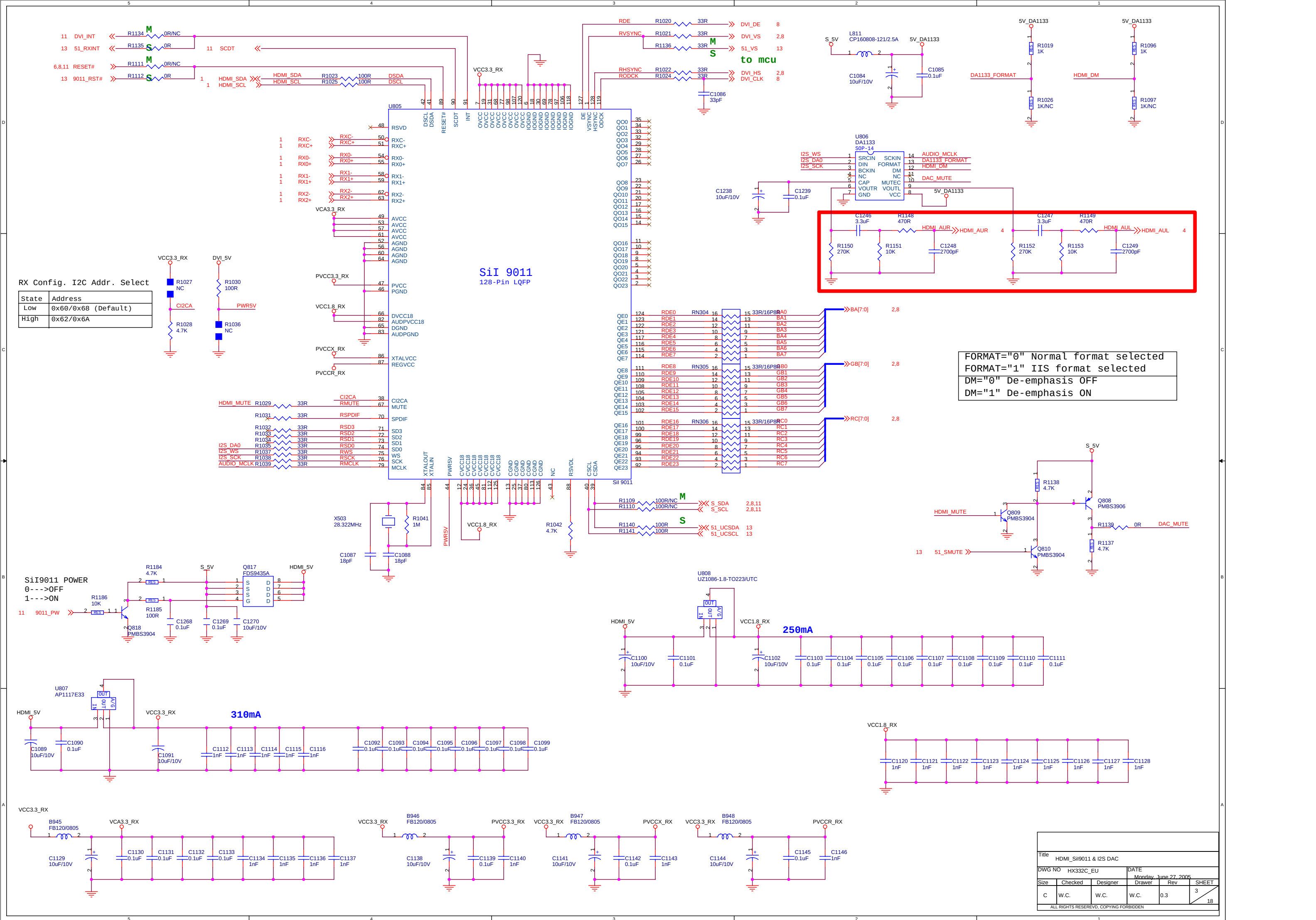


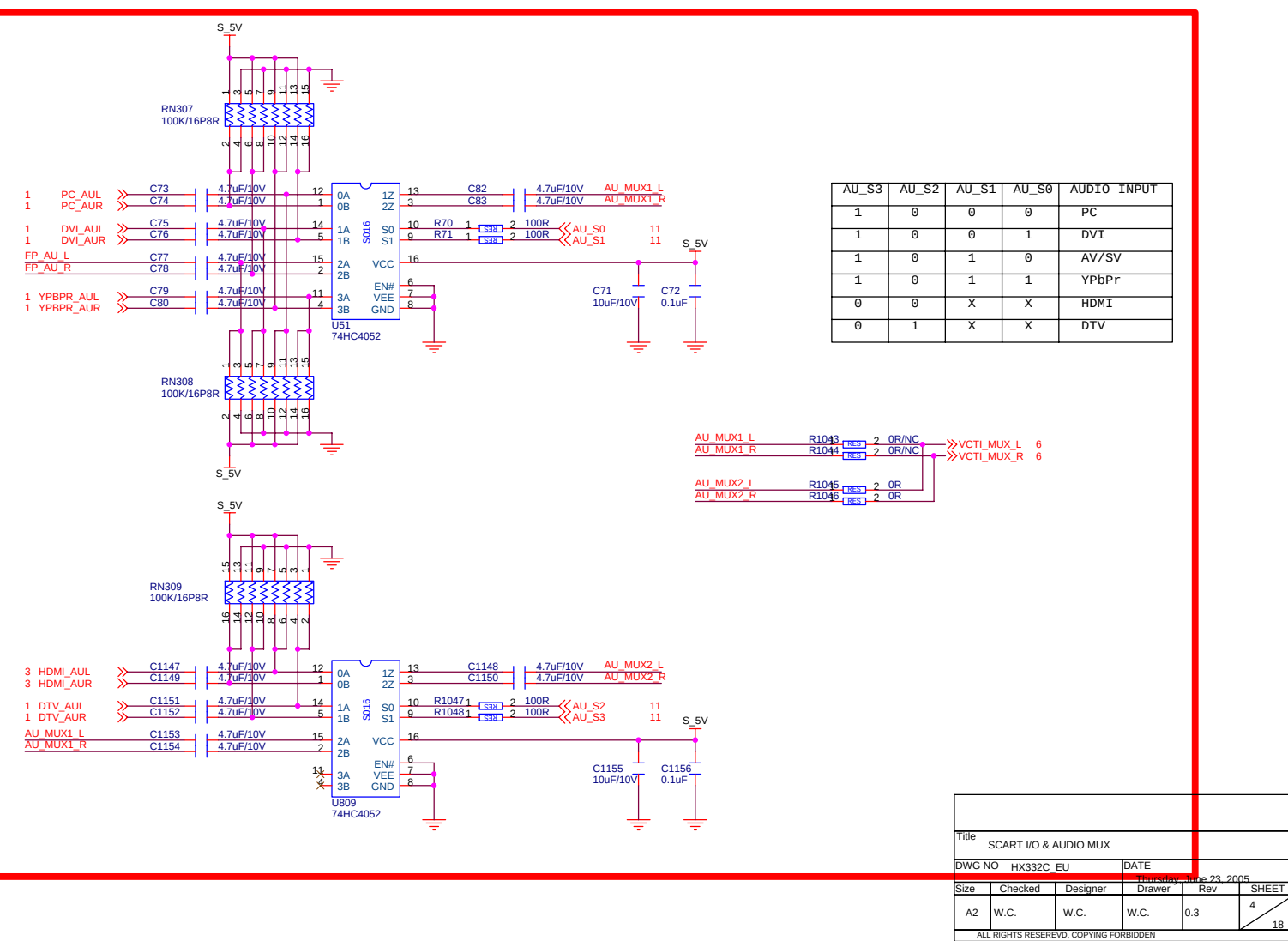
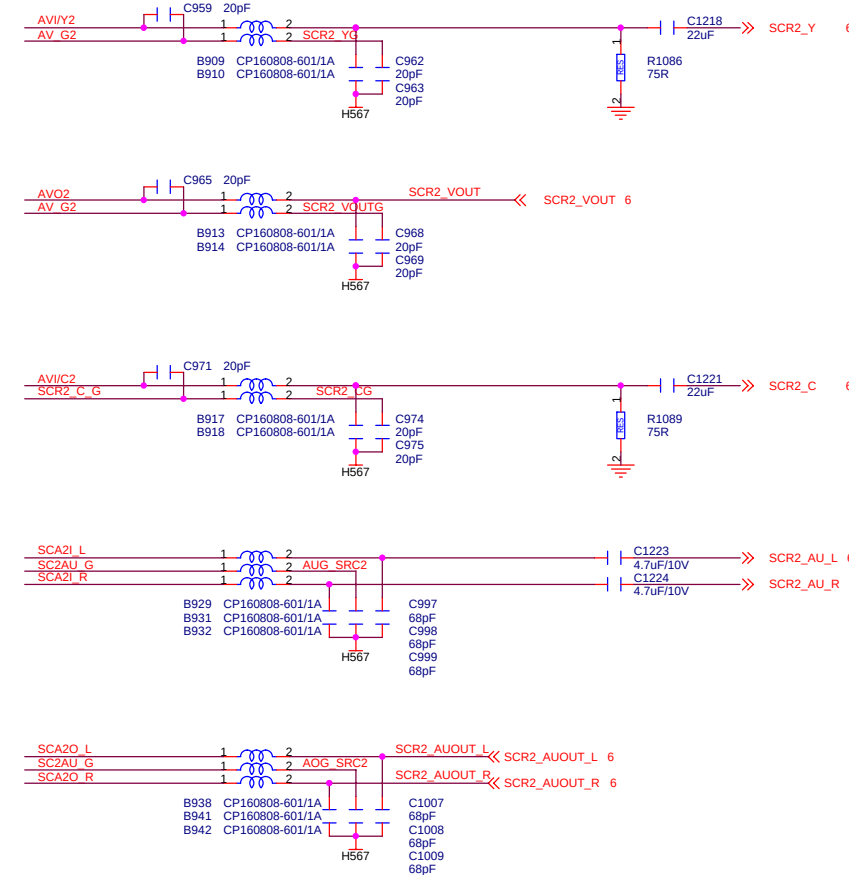
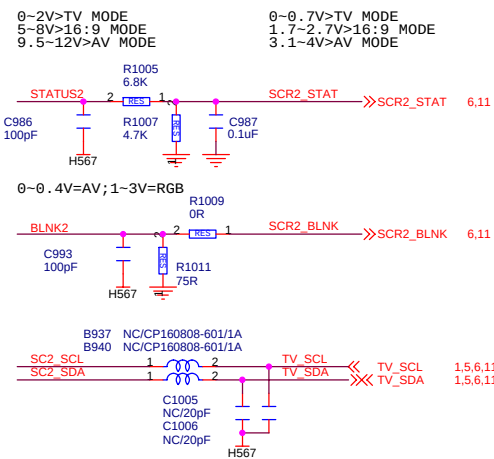
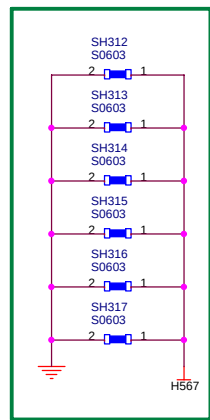
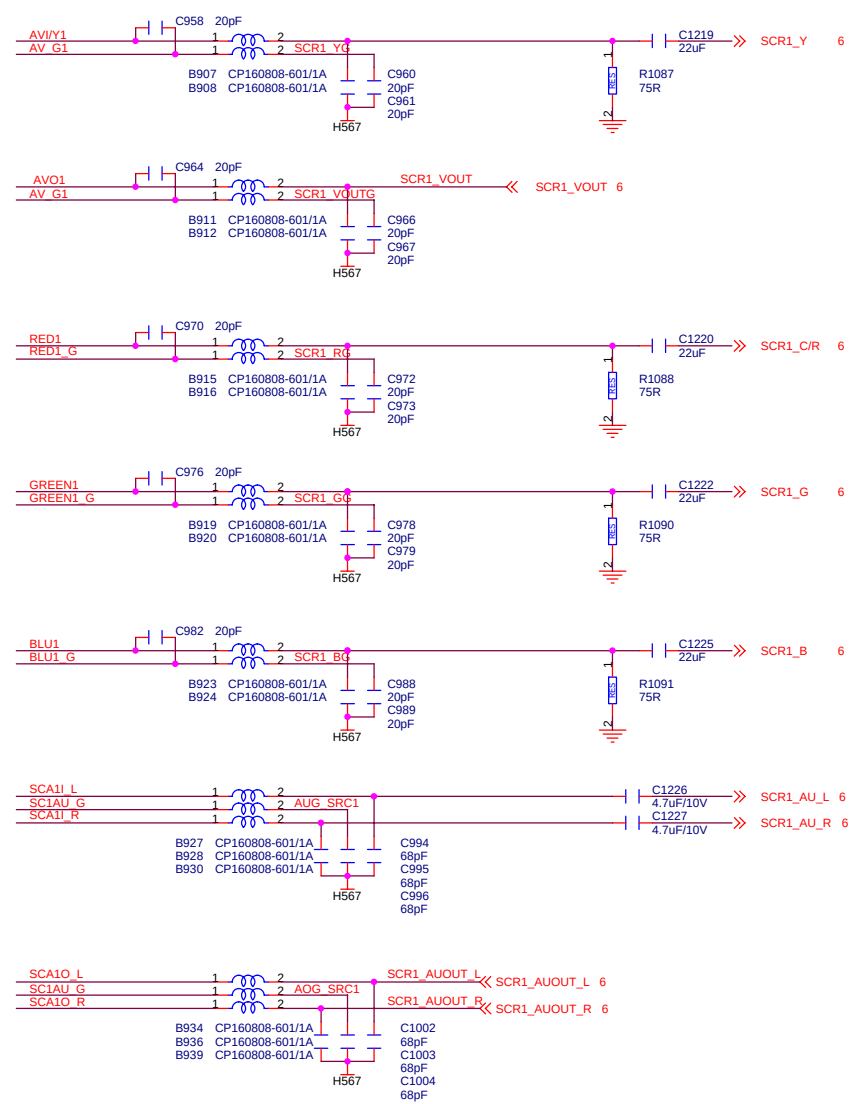
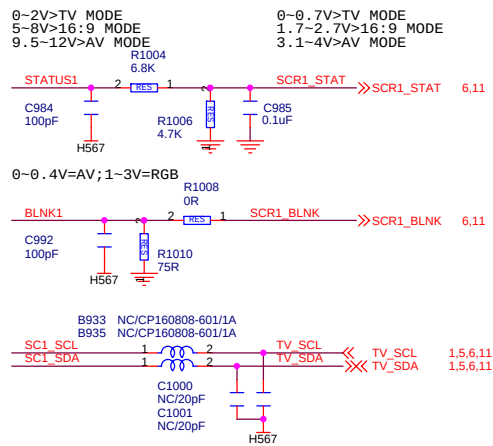
HDMI INTERFACE

DTV/YPBPR MUX (OPTION)

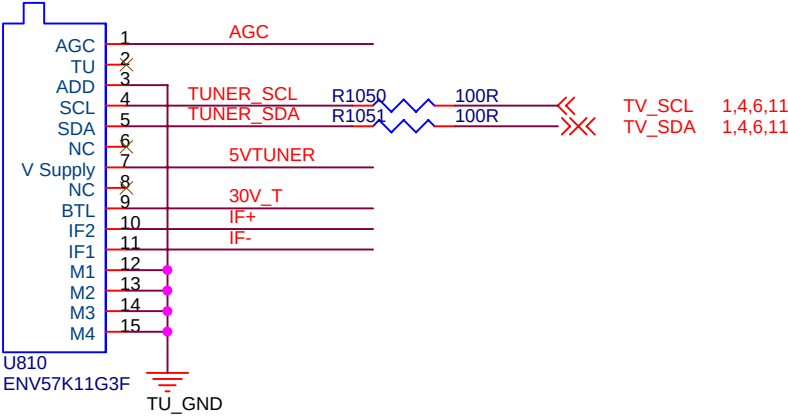


Title MAIN I/O CON			
DWG NO HX332C_EU	DATE Monday, June 27, 2005		
Size Checked Designer	Drawer	Rev	SHEET
A2 W.C.	W.C.	W.C.	0.3 1
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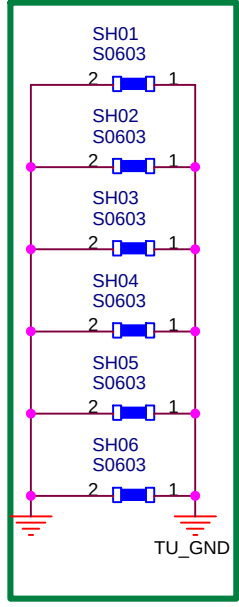
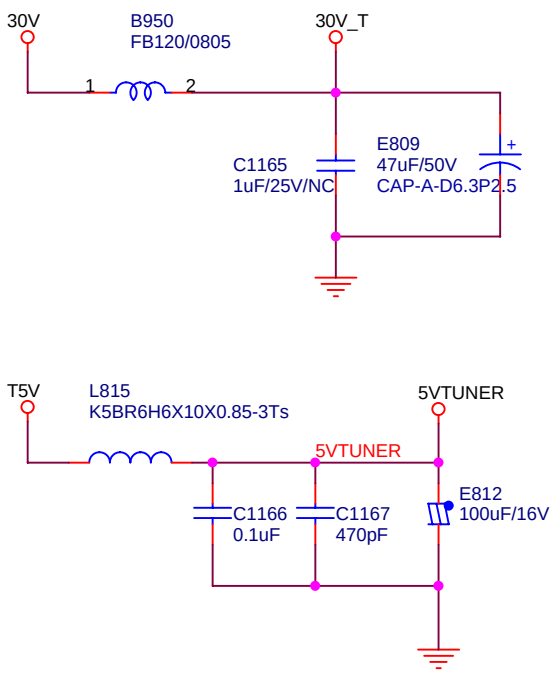
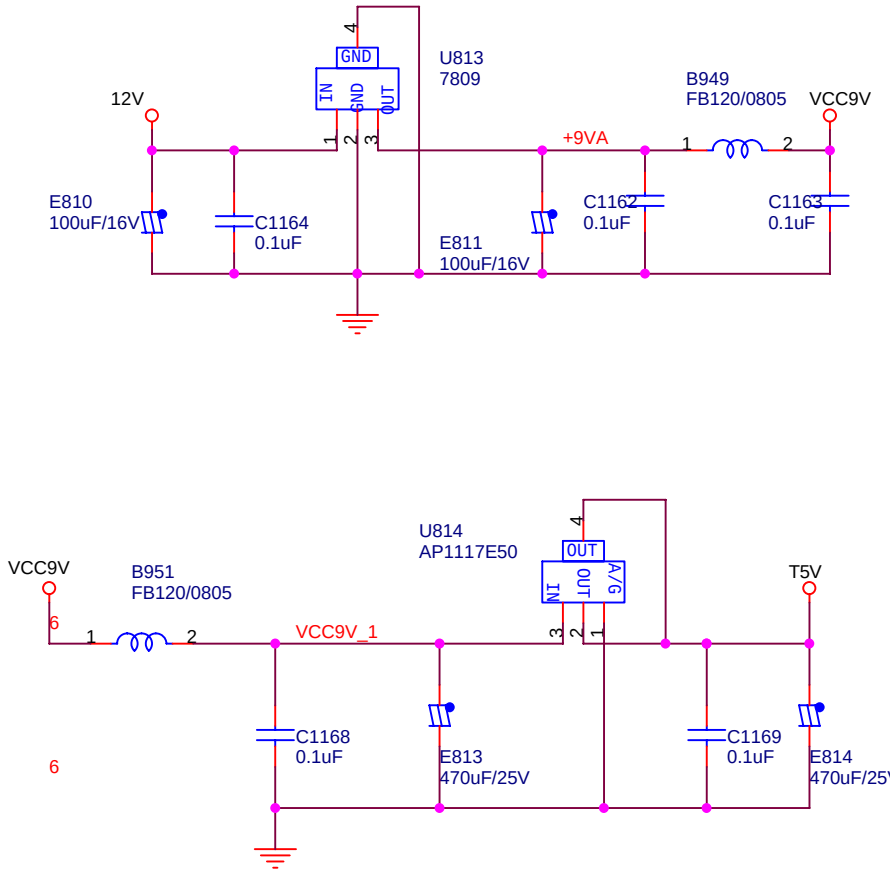
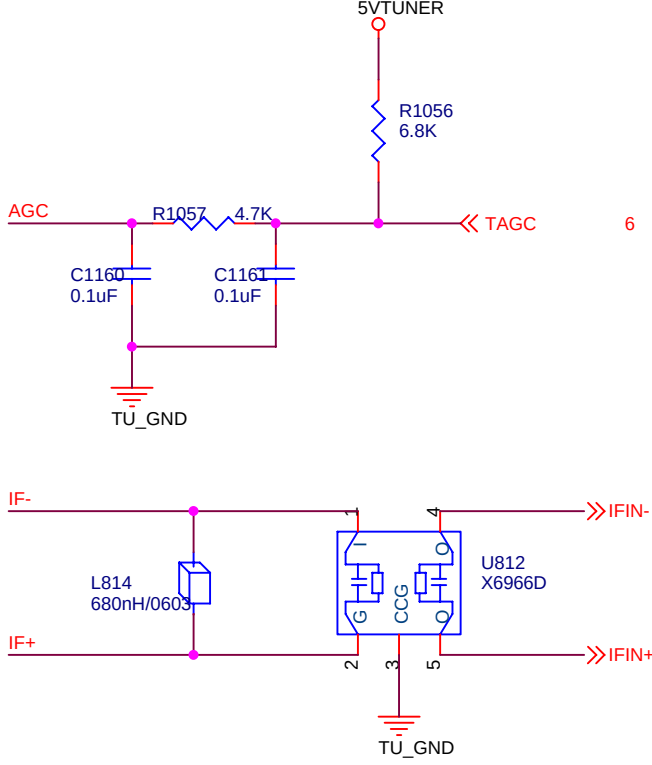
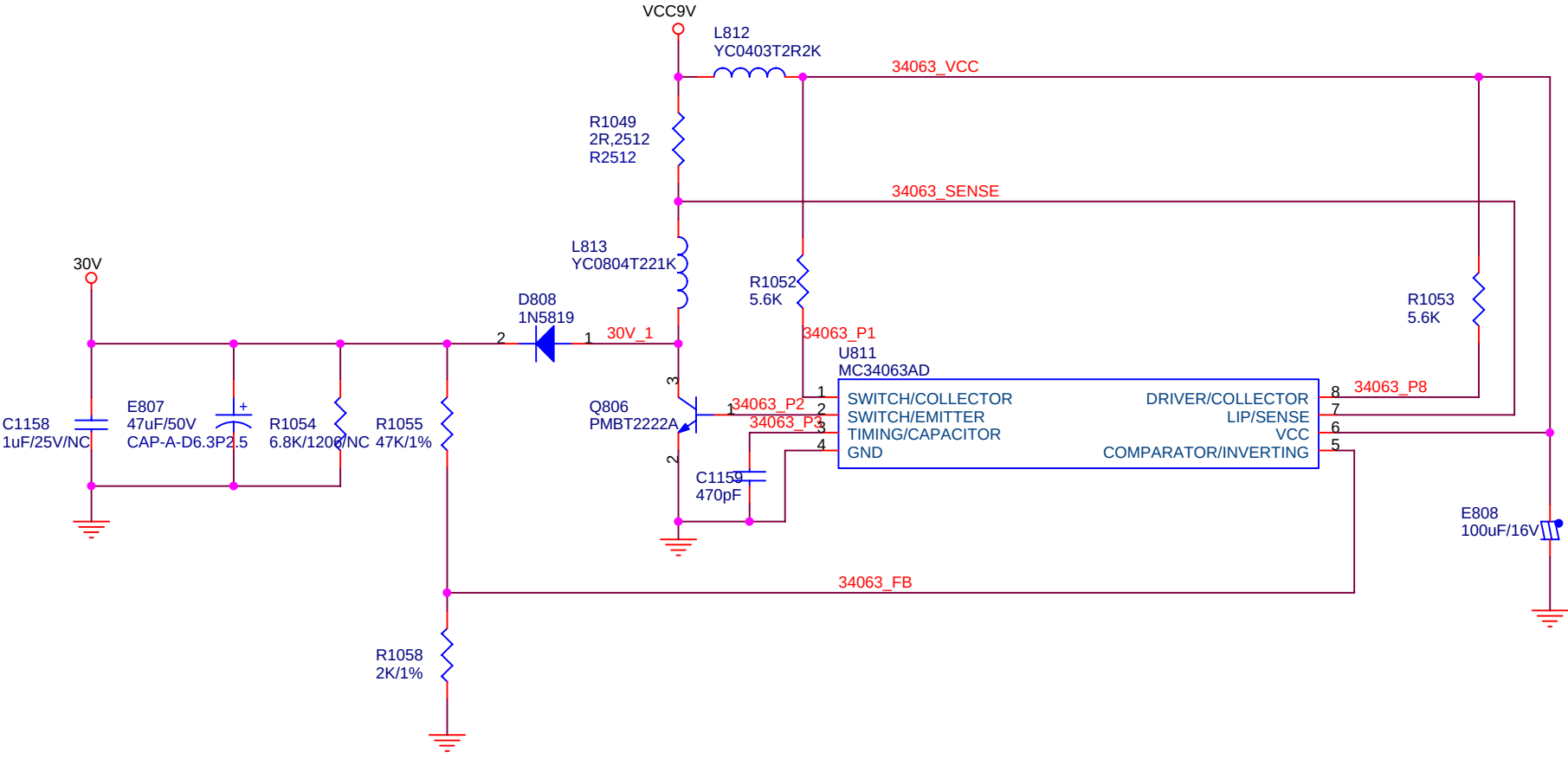
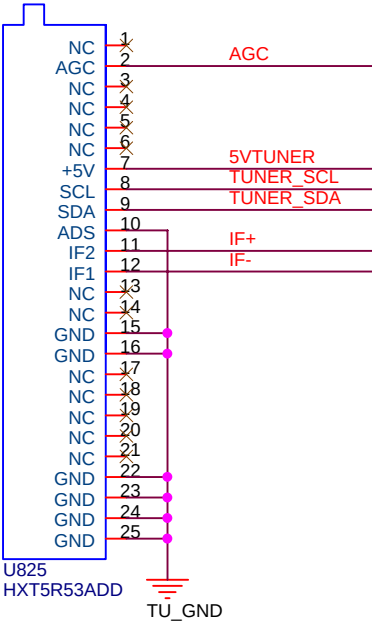




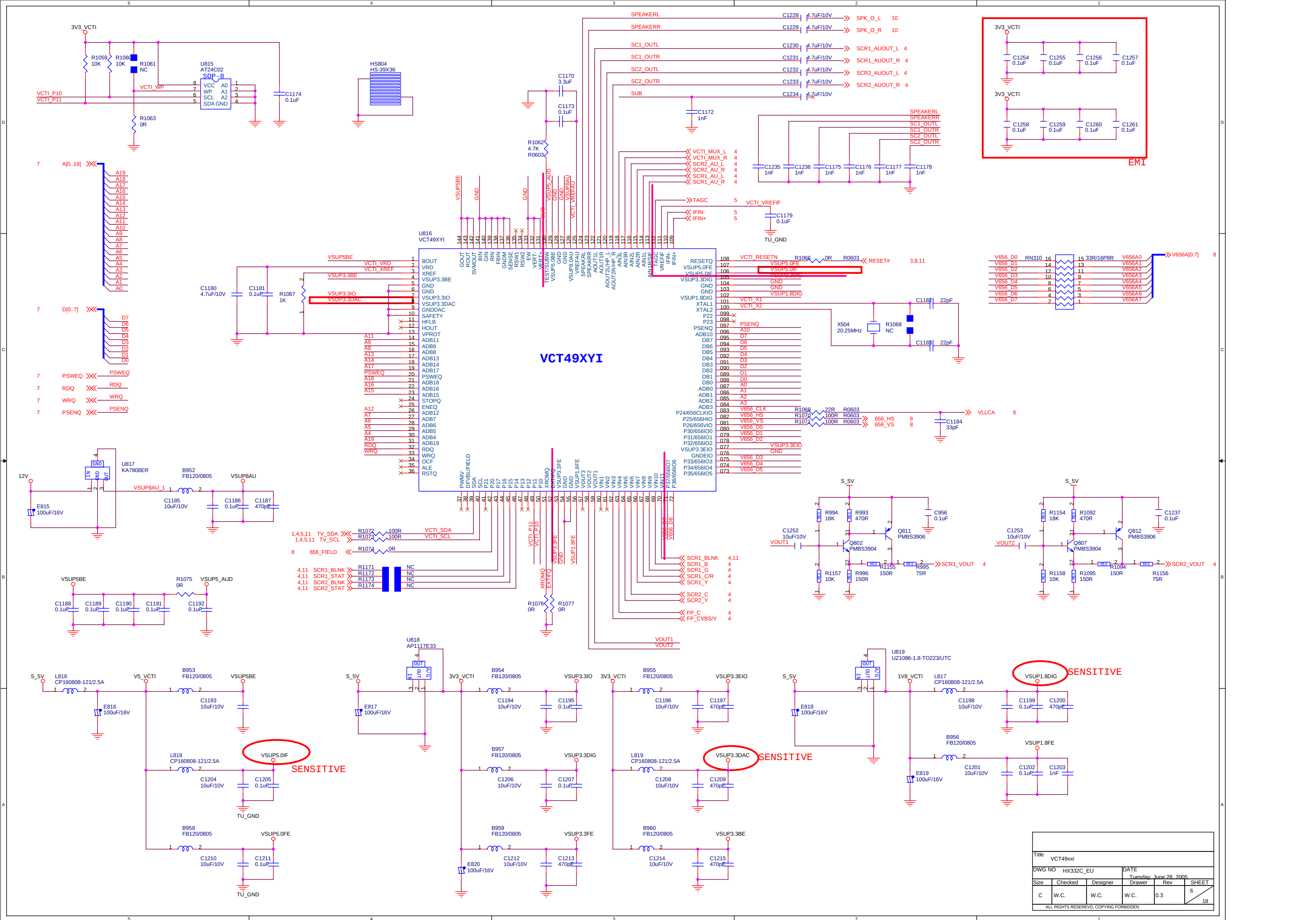
Panasonic Tuner(w/o 30V)
SLAVE Address=0xC0h

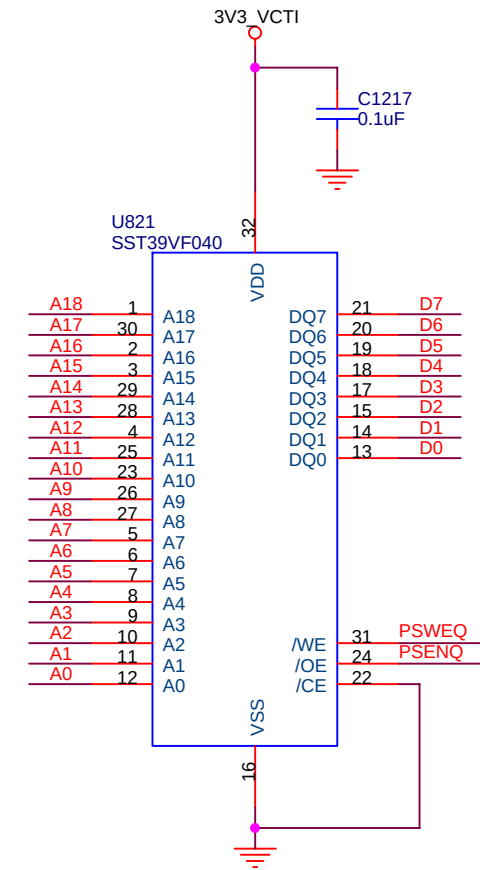
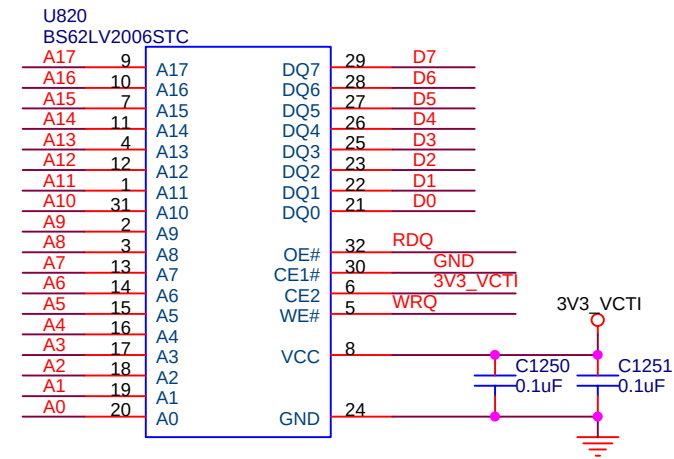
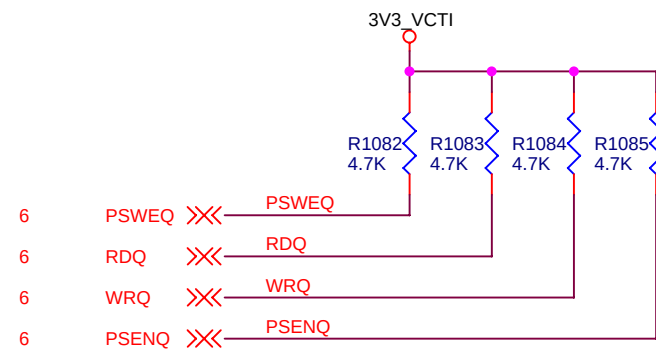
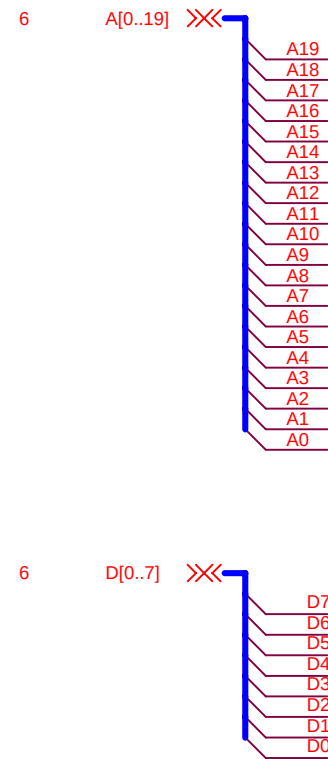


Himax Tuner(with 30V)
SLAVE Address=0x??h



Title TUNER					
DWG NO HX332C_EU			DATE Tuesday, June 28, 2005		
Size	Checked	Designer	Drawer	Rev	SHEET
B	W.C.	W.C.	W.C.	0.3	5
ALL RIGHTS RESERVEVD, COPYING FORBIDDEN					18

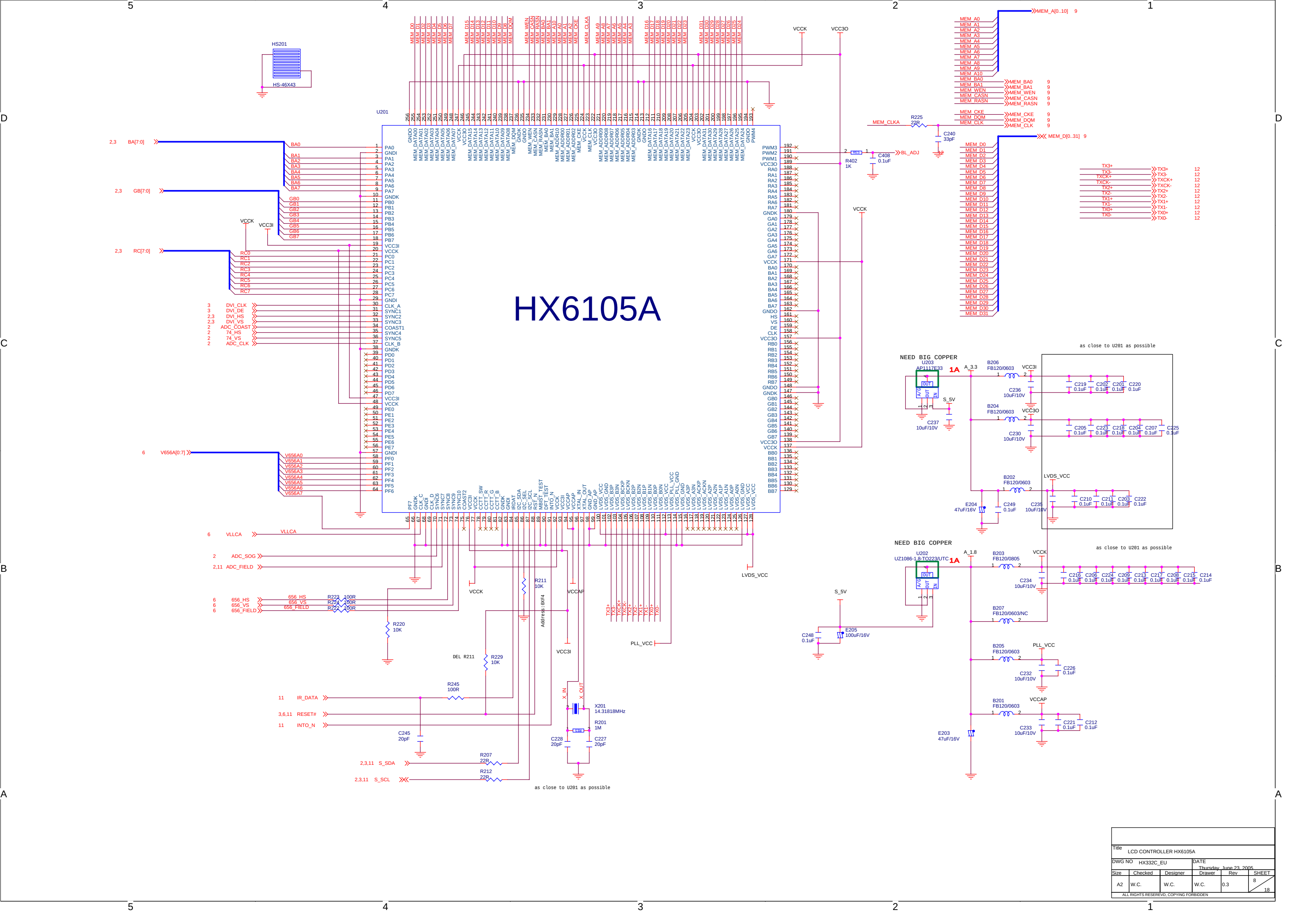




SRAM

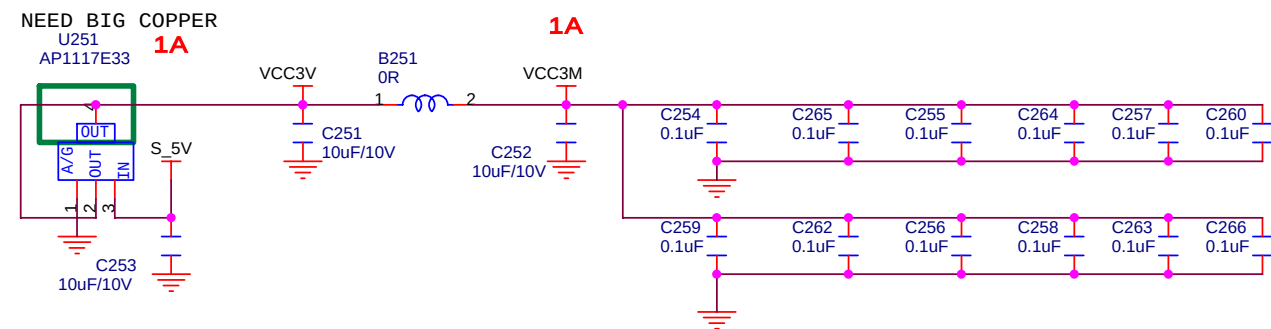
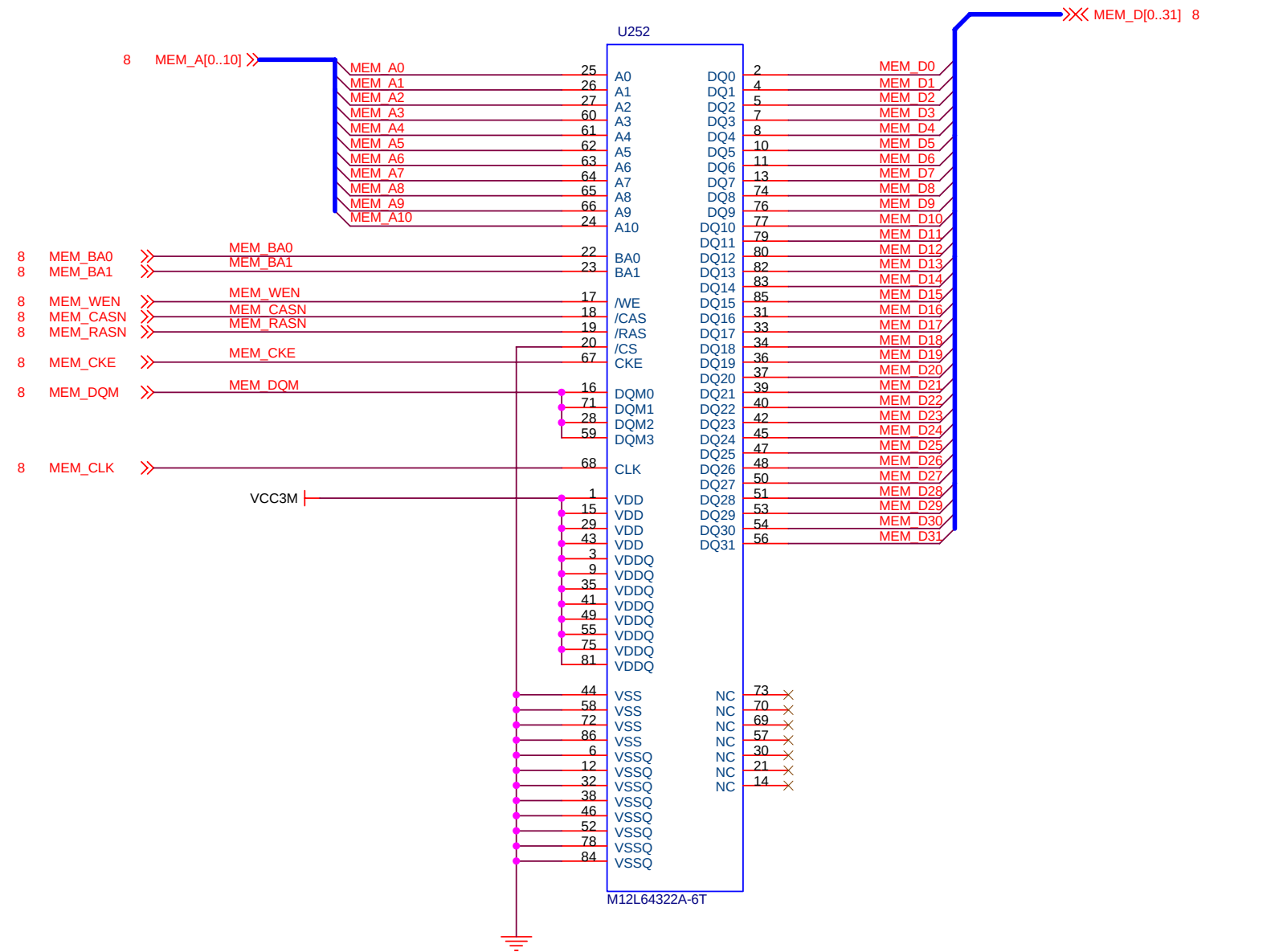
Flash ROM
Add Socket

Title FLASH ROM & SRAM					
DWG NO HX332C_EU			DATE Tuesday, June 28, 2005		
Size	Checked	Designer	Drawer	Rev	SHEET
B	W.C.	W.C.	W.C.	0.3	7 18
ALL RIGHTS RESEREVD, COPYING FORBIDDEN					

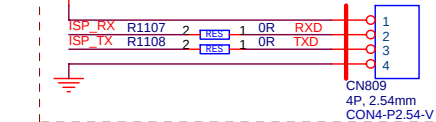
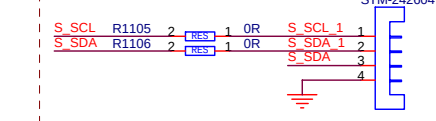
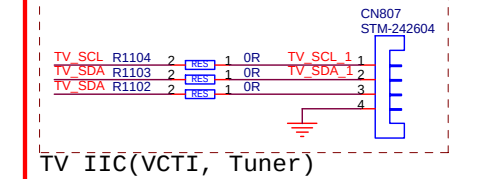
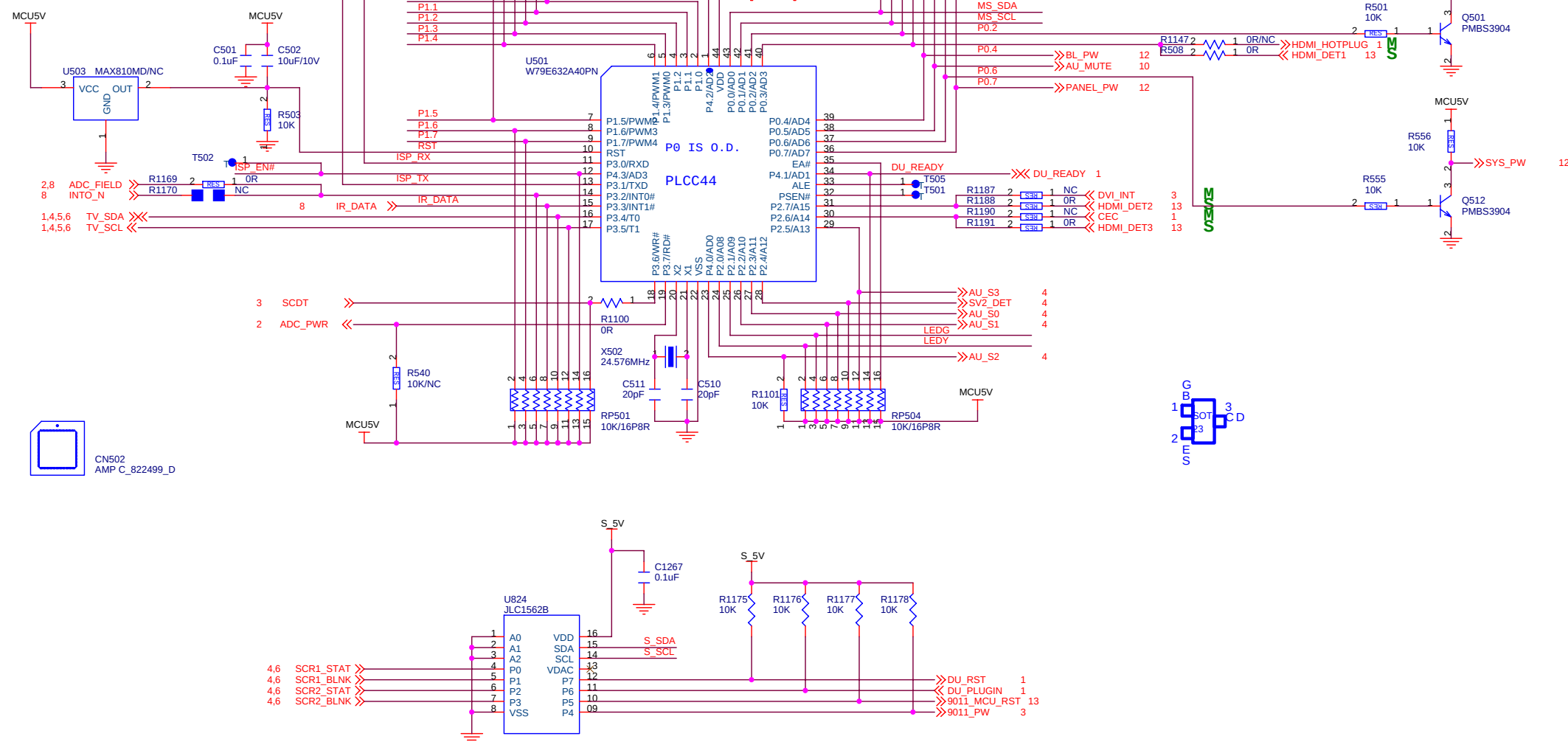
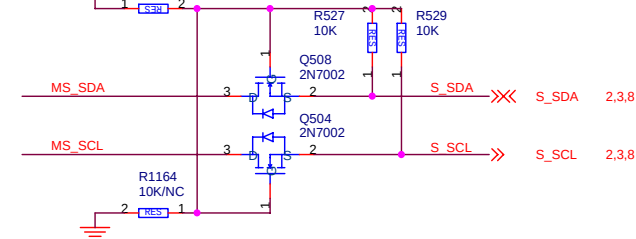
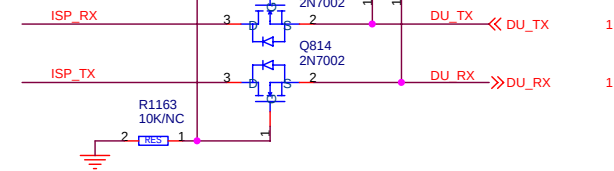
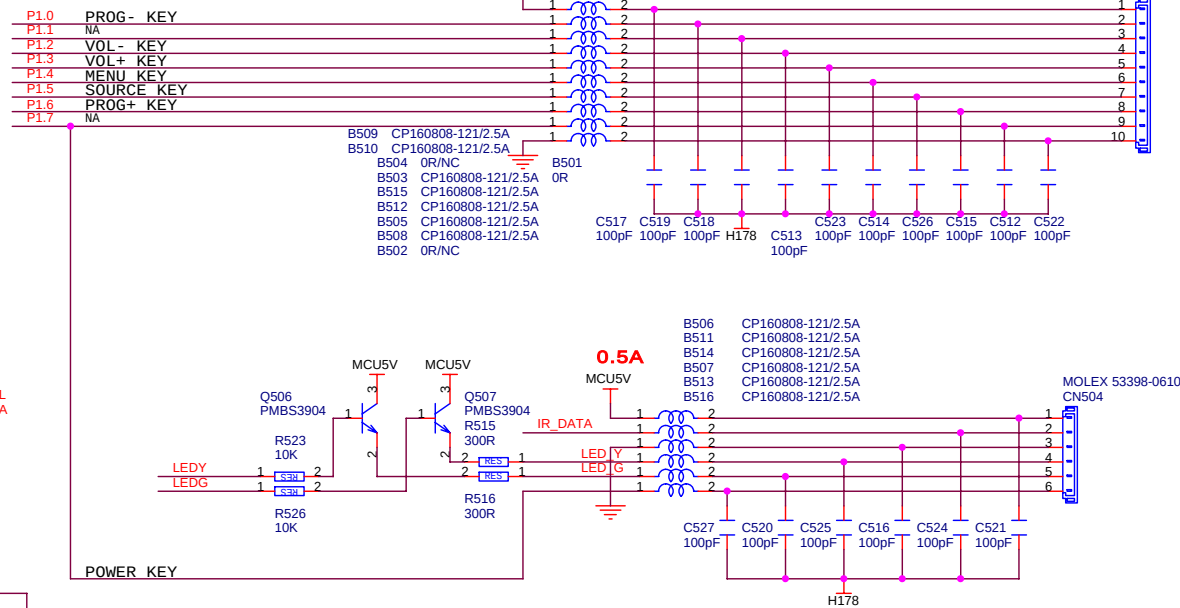
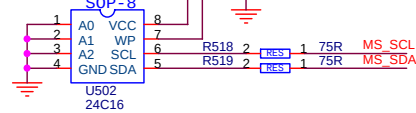


HX6105A

Title					
LCD CONTROLLER HX6105A					
DWG NO		DATE			
HX332C_EU		Thursday, June 23, 2005			
Size	Checked	Designer	Drawer	Rev	SHEET
A2	W.C.	W.C.	W.C.	0.3	8
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Title SDRAM					
DWG NO HX332C_EU			DATE Thursday, June 23, 2005		
Size	Checked	Designer	Drawer	Rev	SHEET
A3	W.C.	W.C.	W.C.	0.3	9
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Title					
MCU					
DWG NO			DATE		
HX332C_EU			Thursday, June 23, 2005		
Size	Checked	Designer	Drawer	Rev	SHEET
C	W.C.	W.C.	W.C.	0.3	11 / 11
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FOR CMO 27", 30" LCD PANEL INTERFACE

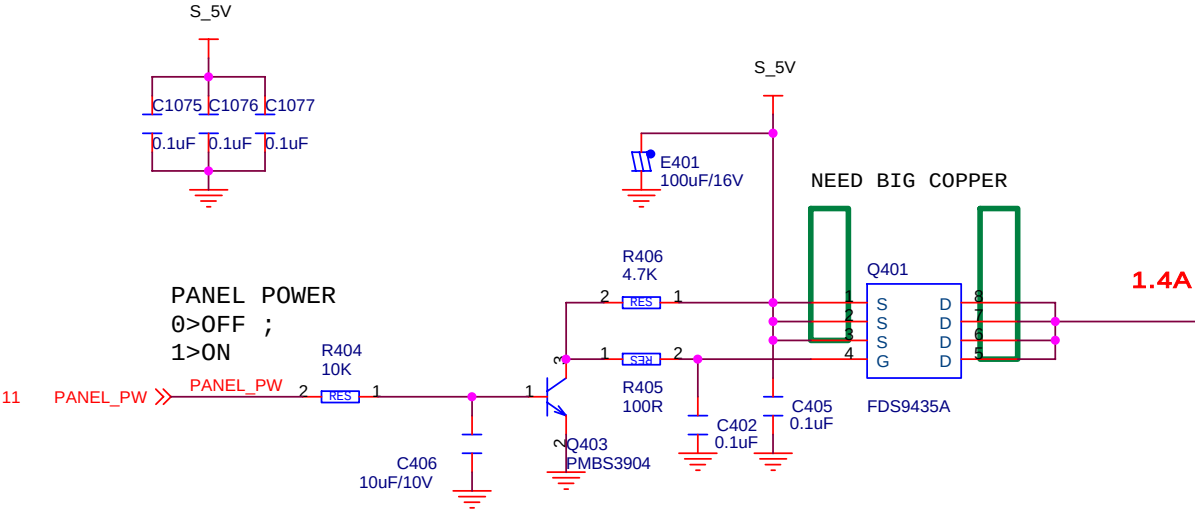
共LAY 線走 W5/L8/W5 mil, LAY1, 線長差 < 14mil, 需平行等長

TX0-	TX0M
TX0+	TX0P
TX1-	TX1M
TX1+	TX1P
TX2-	TX2M
TX2+	TX2P
TXCK-	TXCKM
TXCK+	TXCKP
TX3-	TX3M
TX3+	TX3P

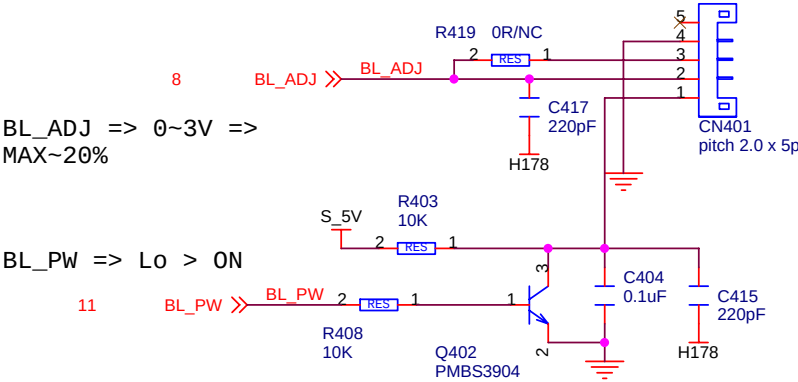
DEL

R409, R410, R418, R416, R413

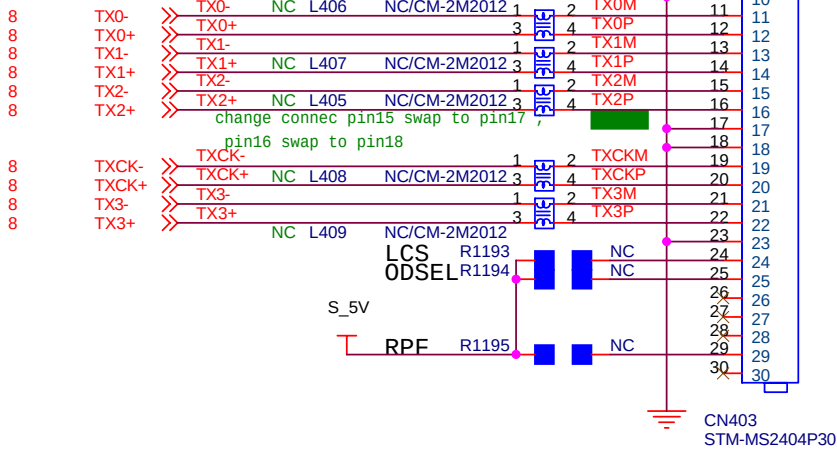
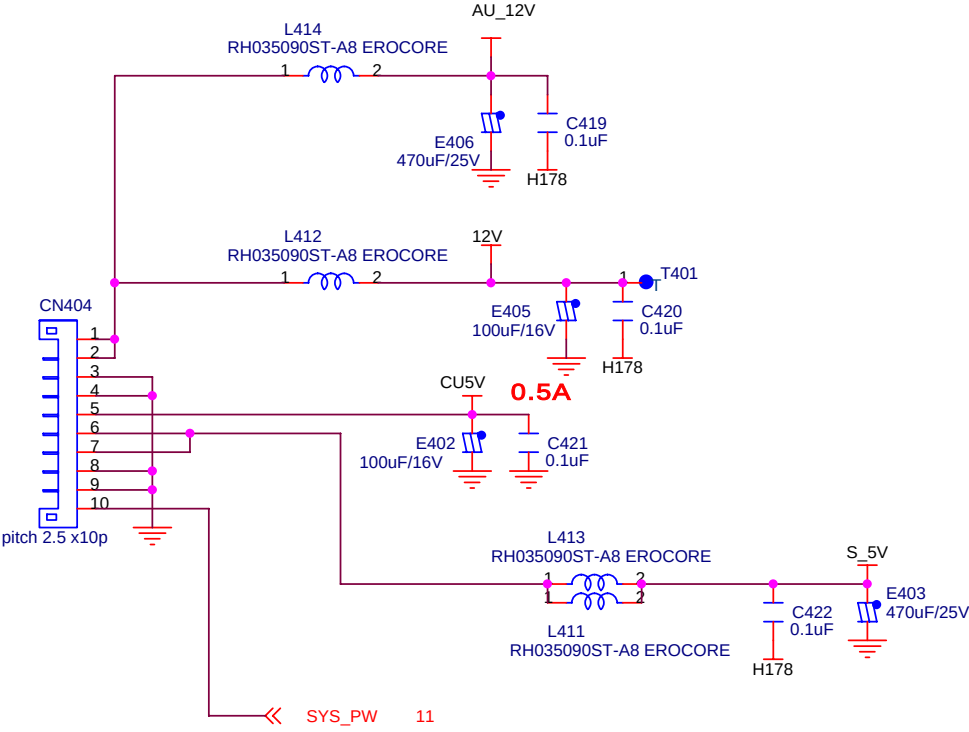
R414, R412, R411, R415, R417



TO INVERTER BOARD



TO POWER BOARD



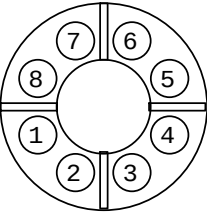
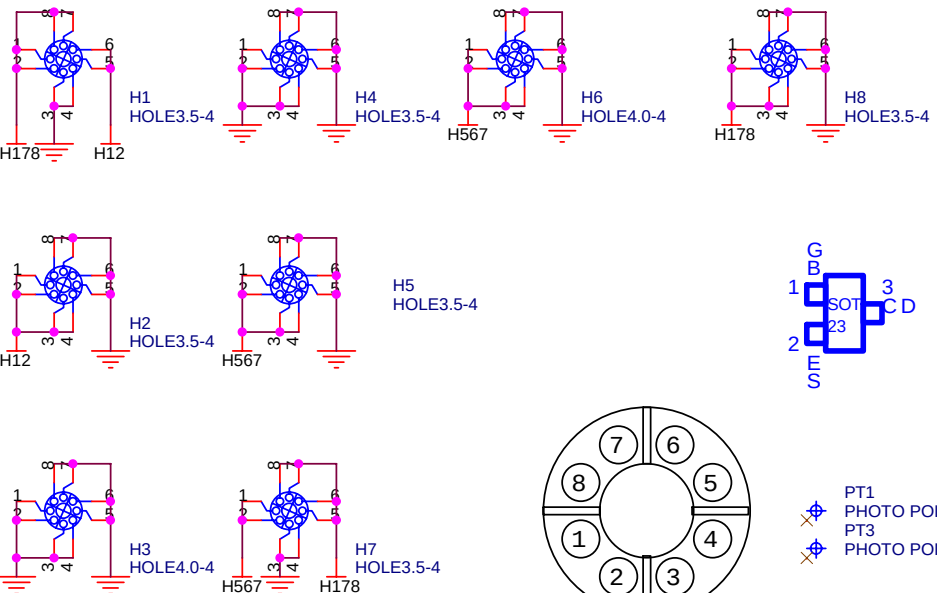
PLUG
STM-P240430

5.1 TFT LCD MODULE

Pin	Name	Description
1	NC	No Connection
2	NC	No Connection
3	NC	No Connection
4	NC	No Connection
5	NC	No Connection
6	NC	No Connection
7	NC	No Connection
8	GND	Ground
9	RX3+	Positive LVDS differential data input, Channel 3
10	RX3-	Negative LVDS differential data input, Channel 3
11	RXCLK+	Positive LVDS differential clock input.
12	RXCLK-	Negative LVDS differential clock input.
13	GND	Ground
14	GND	Ground
15	RX2+	Positive LVDS differential data input, Channel 2
16	RX2-	Negative LVDS differential data input, Channel 2
17	RX1+	Positive LVDS differential data input, Channel 1
18	RX1-	Negative LVDS differential data input, Channel 1
19	RX0+	Positive LVDS differential data input, Channel 0
20	RX0-	Negative LVDS differential data input, Channel 0
21	GND	Ground
22	GND	Ground
23	GND	Ground
24	GND	Ground
25	GND	Ground
26	VCC	+5.0V power supply
27	VCC	+5.0V power supply
28	VCC	+5.0V power supply
29	VCC	+5.0V power supply
30	VCC	+5.0V power supply

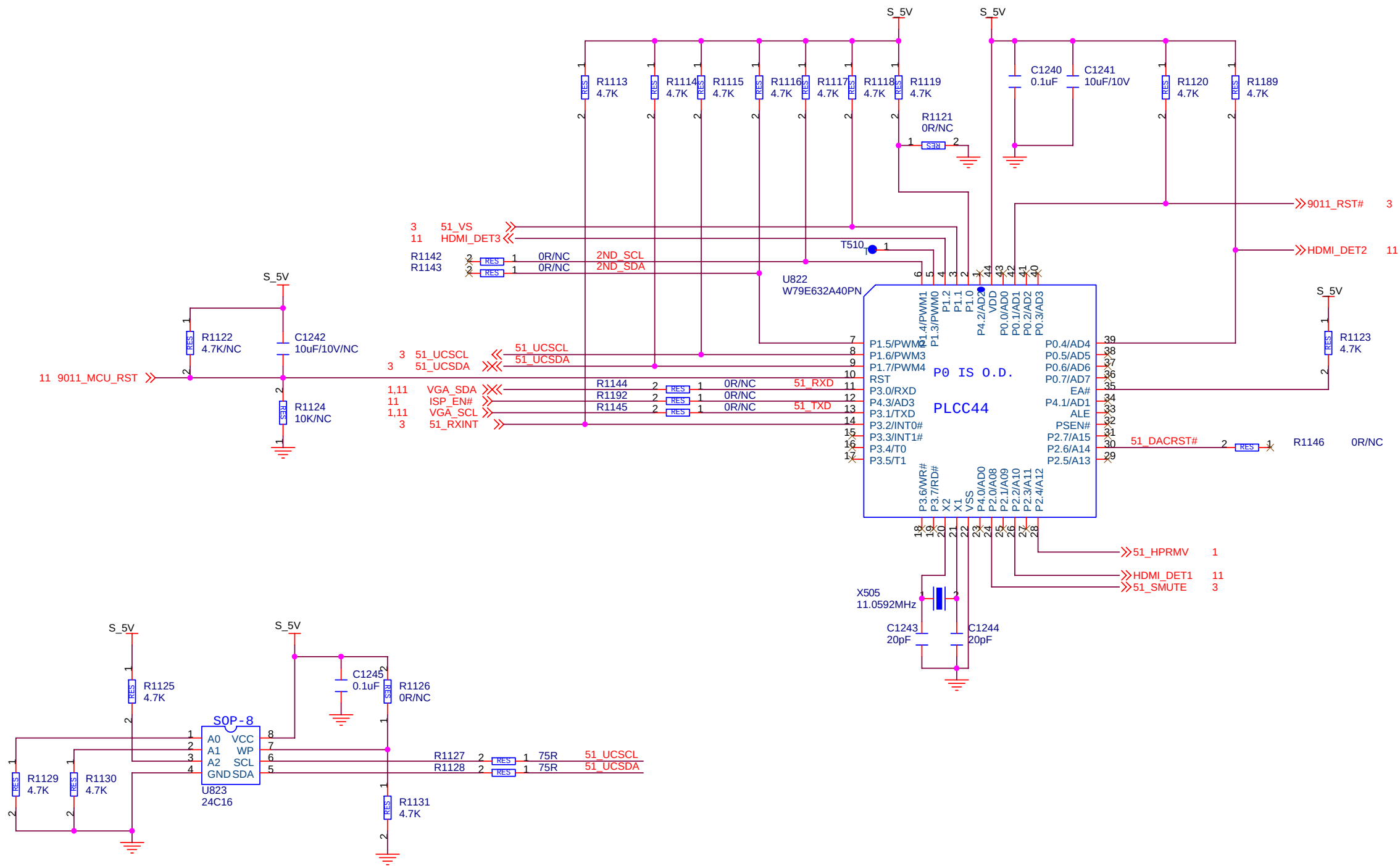
Note (1) Connector Part No.: FI-SE30P-HF (JAE)

Note (2) The first pixel is even.



PT1 PHOTO POINT
PT2 PHOTO POINT
PT3 PHOTO POINT
PT4 PHOTO POINT
PT5 PHOTO POINT

Title PANEL INTERFACE					
DWG NO HX332C_EU			DATE Thursday, June 23, 2005		
Size A3	Checked W.C.	Designer W.C.	Drawer W.C.	Rev 0.3	SHEET 12
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Title 2nd_MCU					
DWG NO HX332C_EU			DATE Thursday, July 07, 2005		
Size	Checked	Designer	Drawer	Rev	SHEET
A3	W.C.	W.C.	W.C.	0.3	13
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Change List (2005/09/19)

Item	Modify	Reason
1	CN404 Need to Mount	CN404 Decal Error (Pin 1)
2	DEL R149/1098/1099	Hardware ISP Disable
3	CN504 Modify To Right-Angle Type/CN809 Error	BOM Error
4	U821 Mount the PLCC32-SKT First	Lost to bulid-into BOM
5	R139, R143, R145 Mount 0 ohm (U103 Unmount)	YPbPr Direct Input
6	R815 Change to 4.7K ohm	TPA3004 Amplifier VOL Change
7	U804 Change to 24C02 (or 24C04/08/16)	Support DDC2
8	R1010/1011 Change to 75 ohm R1004/1005 Change to 6.8K ohm R1006/1007 Change to 4.7K ohm	Fine Tune SCART Detect Voltage
9	Connect L411.2 to E805.1(S_5V to AU_5V) ADD B961(FB120/0805)	Schematic Error (Lost U802 AU_5V Input)
10	R843/844 Remove R817/819 Change to 100K ohm	Fine Tune EarPhone VOL
11	Modify CN807 Pin Define	For CN807 Pin Define the same with CN808
12	DEL R559 ADD U824, R1175/1176/1177/1178(10K ohm) ADD C1267(0.1uF) ADD R1171/1172/1173/1174(NC)	ADD I2C Expander
13	ADD HeatSink HS804 for VCTi	Reserve
14	ADD Q815/816 (PMBS3904) ADD R1179/1180 (10K ohm), R1181 (1M ohm)	Hardware ISP Function
15	ADD R1159 (0R)	HDMI CEC ENABLE
16	DEL R1014 (75R) ADD R1182/1183 (10K) R121 CONNECT TO GND	EEPROM FUNCTION
17	DEL R1122, R1124, C1242	2ND MCU RESET DISABLE
18	CN106 Change to CON12-P2	CHANGE BOM
19	R1041 1M ohm	SiI9011 CRYSTAL BALANCE
20	ADD R1186(10K), R1184(4.7K), R1185(100R) C1268(0.1uF), c1269(0.1uF), C1270(10uF) Q818(PMBS3904), Q817(FDS9435A)	SiI POWER CONTROL
21	DEL R825, D806, E806	TPA3004 FADE MODE
22	DEL R823, R801, Q801	AU_MUTE
23	DEL R810, R816 ADD R812, R814 (1K)	TPA3004 Setup
24	ADD R1187(NC), R1188(0R), R1189(4.7K)	HDMI Interlace/Progressive Detect
25	SDRAM ESMT M12L64322A-6T(166MHz)	1080i with PIP need speed up 155MHz

Change List (2005/10/07)

Item	Modify	Reason
1	DEL C1158, C1165, R1054	Component NOT support 30V
2	U820 Change to 2M SRAM	Teletext 100page Change to 252page
3	ADD R1190/R1192(NC), R1191(0R)	HDMI DETECT
4	ADD U825(Dual Layout with U810)	Dual Layout Himax & Panasonic Tuner
5	U815 Chang to AT24C02	Cost Down & Function Ready
6	R121 CONNECT TO VGA_VS	VGA EDID SUPPORT DDC1
7	R1144/1145/1192 CONNECT TO MAIN MCU	RESERVE FOR TEST

Change List (2005/11/10)

Item	Modify	Reason
1	CN23, CN401 Change CONNECTOR	Assembly Smooth
2	ADD R1193, R1194, R1195(NC) FOR CMO 27 PANEL	

Change List (2005/12/22)

Item	Modify	Reason
1	SCART PIN 16 connector to VCTI for FB	CANAL+ NEEDED

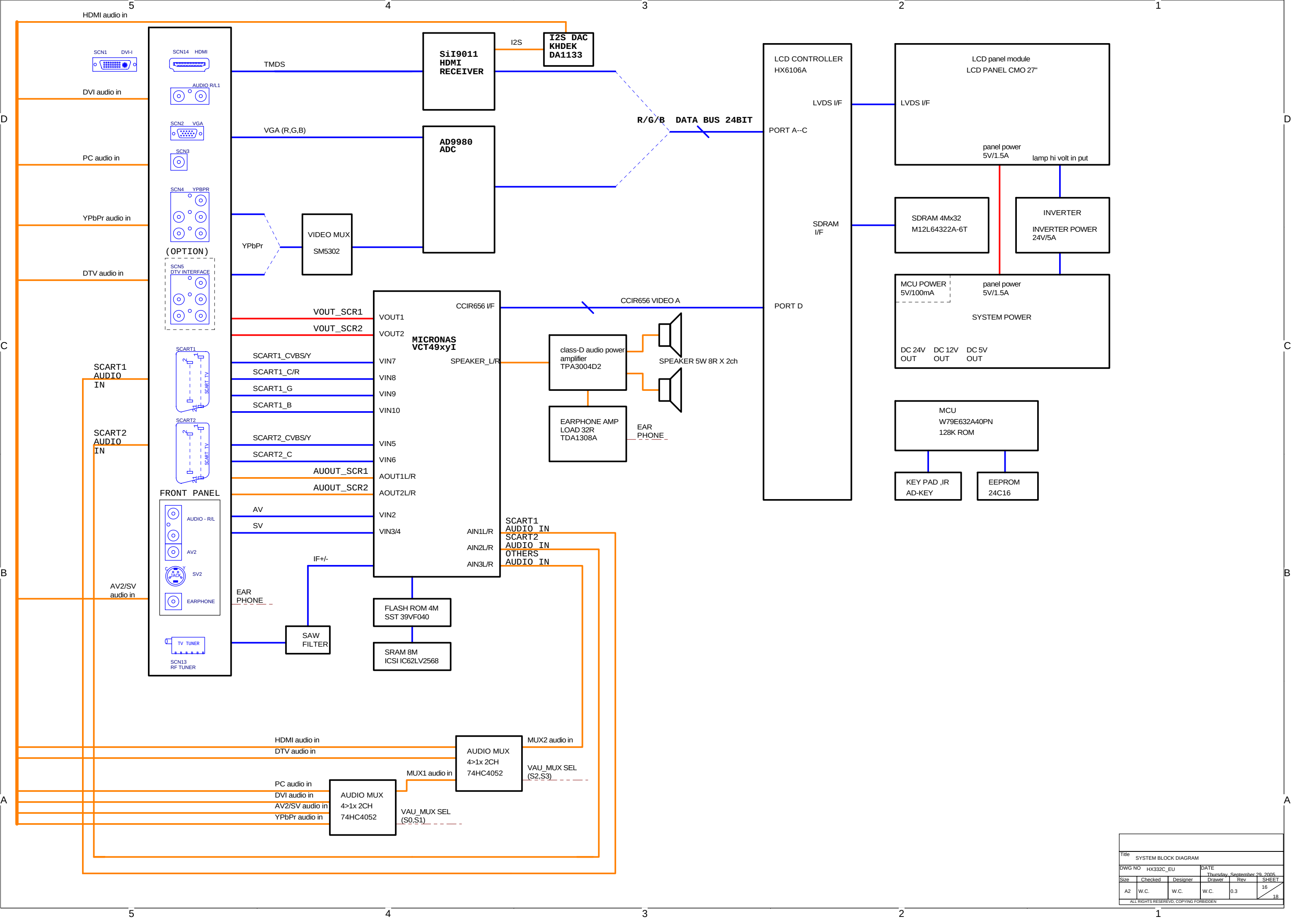
Title History					
DWG NO HX332C_EU			DATE Wednesday, September 07, 2005		
Size	Checked	Designer	Drawer	Rev	SHEET
C	W.C.	W.C.	W.C.	0.3	14 18
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UPDATE LIST		
NOTE		
"NC" FOR NO MOUNT PART		

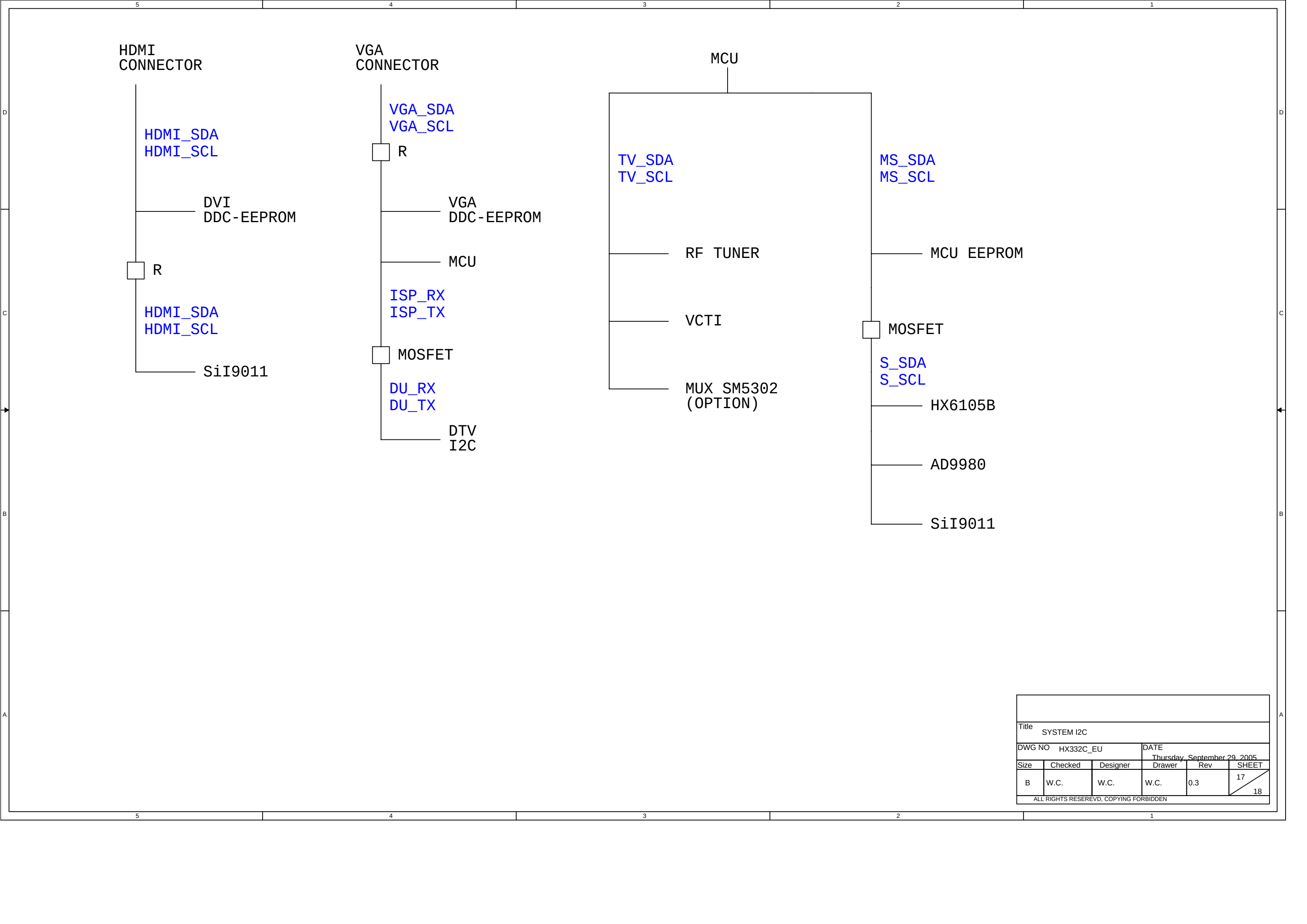
Draw	Check	Approve

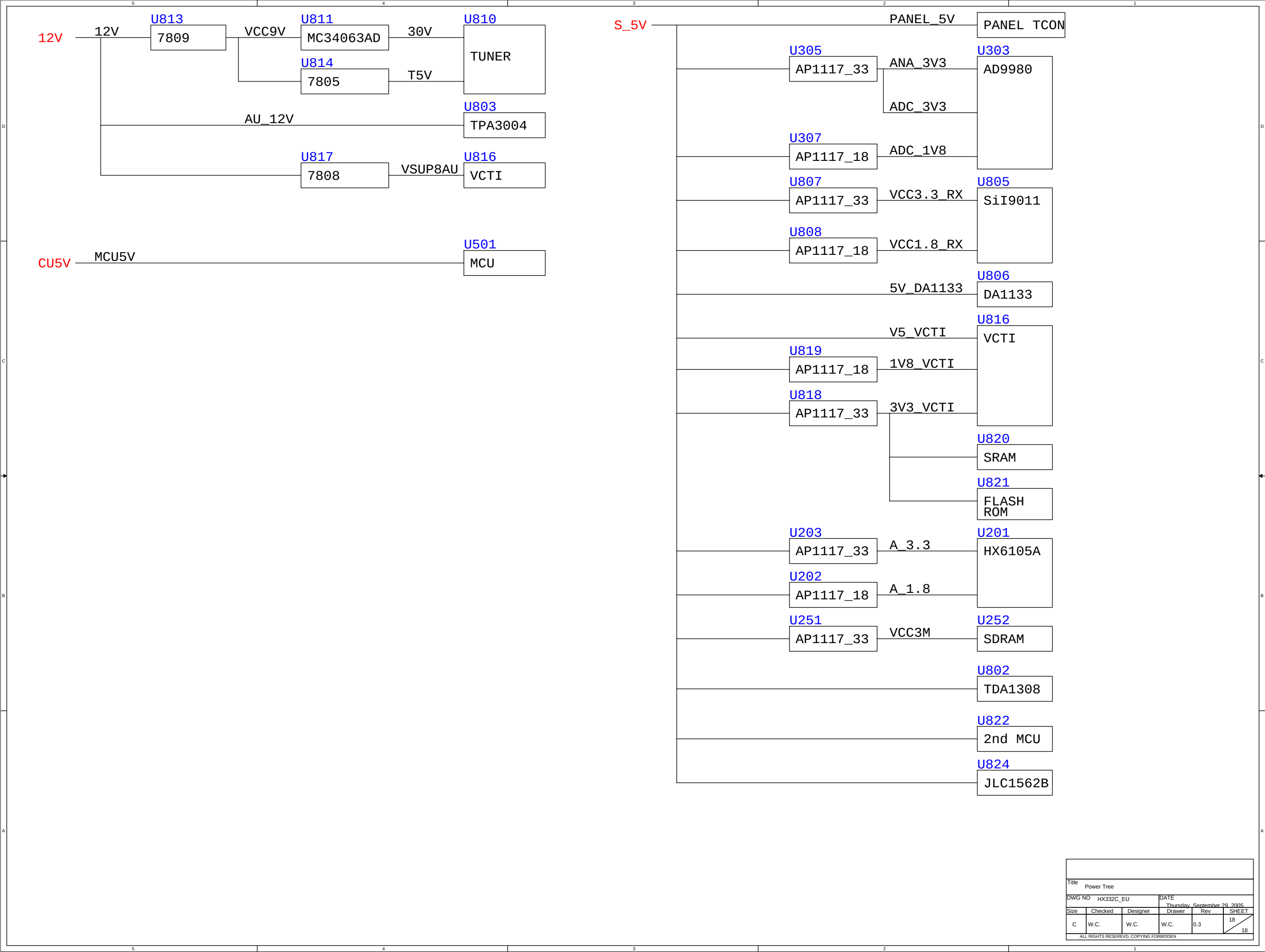
Title					
Cover Page					
DWG NO			DATE		
HX332C_EU			Thursday, September 29, 2005		
Size	Checked	Designer	Drawer	Rev	SHEET
C	W.C.	W.C.	W.C.	0.3	15 / 18
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Page List
01 MAIN I/O CON
02 AD9880
03 HDMI SiI9011 & I2S DAC
04 SCART I/O & AUDIO MUX
05 Tuner
06 VCT4982i
07 FLASH ROM & SRAM
08 LCD CONTROLLER HX6105
09 SDRAM
10 AUDIO AMPLIFIER TPA3004D2
11 MCU
12 PANEL IF & POWER
13_2nd MCU for SiI9011
14_History
A_Cover Page
B_SYSTEM BLOCK DIAGRAM
C_I2C
D_Power Tree



Title					
SYSTEM BLOCK DIAGRAM					
DWG NO		DATE			
HX332C_EU		Thursday, September 29, 2005			
Size	Checked	Designer	Drawer	Rev	SHEET
A2	W.C.	W.C.	W.C.	0.3	16
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Title					
Power Tree					
DWG NO			DATE		
HX332C_EU			Thursday, September 29, 2005		
Size	Checked	Designer	Drawer	Rev	SHEET
C	W.C.	W.C.	W.C.	0.3	18
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